

IN THE CLAIMS:

1. (Currently Amended) A display device comprising:
- a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a digital video signal dividing circuit, a source driver circuit, and a gate driver circuit;
- an image signal processing circuit for processing an image signal input from an external source; and
- a control circuit which directly feeds pulses to said ~~display panel~~ digital video signal dividing circuit, said source driver circuit, said gate driver circuit and said image signal processing circuit,
- wherein said image signal processing circuit corrects said image signal on a basis of a correction table and feeds said digital video signal dividing circuit with said a corrected image signal [[.]], and
- wherein said digital video signal dividing circuit feeds said source driver circuit with a digital video signal.
2. (Previously Presented) A display device according to claim 1, wherein said display panel is a liquid crystal display panel.
3. (Original) A display device according to claim 1, wherein said source driver circuit is a digital driver with a D/A conversion circuit.
4. (Original) A display device according to claim 1, wherein said image signal processing circuit comprises a correction circuit and an A/D conversion circuit.
5. (Original) A display device according to claim 1, wherein said display device is one selected from the group consisting of a projector, a goggle type display, a mobile computer, a video camera, a DVD player, and a game machine.
6. (Currently Amended) A display device comprising:

a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a digital video signal dividing circuit, a source driver circuit, and a gate driver circuit;

an image signal processing circuit for processing an image signal input from an external source; and

a control circuit which directly feeds pulses to said ~~display panel~~ digital video signal dividing circuit, said source driver circuit, said gate driver circuit and said image signal processing circuit,

wherein said image signal processing circuit performs gamma correction on said image signal on a basis of a correction table and feeds said digital video signal dividing circuit with said image signal on which gamma correction has been performed[[]], and

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Cmt wherein said digital video signal dividing circuit feeds said source driver circuit with a digital video signal.

7. (Original) A display device according to claim 6, wherein said display panel is a liquid crystal display panel.

8. (Original) A display device according to claim 6, wherein said source driver circuit is a digital driver with a D/A conversion circuit.

9. (Original) A display device according to claim 6, wherein said image signal processing circuit comprises a correction circuit and an A/D conversion circuit.

10. (Original) A display device according to claim 6, wherein said display device is one selected from the group consisting of a projector, a goggle type display, a mobile computer, a video camera, a DVD player, and a game machine.

11. (Currently Amended) A method for operating a display device comprising the steps of:

processing an image signal input from an external source by an image signal processing circuit;

feeding pulses directly to said image signal processing circuit ~~and a display panel~~, a digital video signal dividing circuit, a source driver circuit and a gate driver circuit by a control circuit, wherein ~~the~~ a display panel comprises a pixel portion in which a plurality of thin film transistors are arranged in a matrix, [[a]] said digital video signal dividing circuit, [[a]] said source driver circuit, and [[a]] said gate driver circuit;

correcting said image signal based on a correction table; ~~and~~

feeding a corrected image signal to said digital video signal dividing circuit through a correction circuit~~[[.]]; and~~

feeding a digital video signal to said source driver circuit through said digital video signal dividing circuit.

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12. (Original) A method according to claim 11, wherein said display device is a liquid crystal display device.

13. (Original) A method according to claim 11, wherein said display device is one selected from the group consisting of a projector, a goggle type display, a mobile computer, a video camera, a DVD player, and a game machine.

14. (Currently Amended) A method for operating a display device comprising the steps of:

processing an image signal input from an external source by an image signal processing circuit;

feeding pulses directly to said image signal processing circuit ~~and a display panel~~, a digital video signal dividing circuit, a source driver circuit and a gate driver circuit by a control circuit, wherein ~~the~~ a display panel comprises a pixel portion in which a plurality of thin film transistors are arranged in a matrix, [[a]] said digital video signal dividing circuit, [[a]] said source driver circuit, and [[a]] said gate driver circuit;

performing a gamma correction of said image signal based on a correction table; and

feeding a corrected image signal to said digital video signal dividing circuit through a correction circuit[.]; and

feeding a digital video signal to said source driver circuit through said digital video signal dividing circuit.

15. (Original) A method according to claim 14, wherein said display device is a liquid crystal display device.

16. (Original) A method according to claim 14, wherein said display device is one selected from the group consisting of a projector, a goggle type display, a mobile computer, a video camera, a DVD player, and a game machine.

17. (Currently Amended) A display device comprising:

a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a digital video signal dividing circuit, a source driver circuit, and a gate driver circuit, wherein each circuit is formed over a same substrate as said pixel portion;

an image signal processing circuit for processing an image signal input from an external source; and

a control circuit which directly feeds pulses to said ~~display panel~~ digital video signal dividing circuit, said source driver circuit, said gate driver circuit and said image signal processing circuit,


wherein said image signal processing circuit corrects said image signal on a basis of a correction table and feeds said digital video signal dividing circuit with ~~said~~ a corrected image signal[.], and

wherein said digital video signal dividing circuit feeds said source driver circuit with a digital video signal.

18. (Previously Presented) A display device according to claim 17, wherein said display panel is a liquid crystal display panel.

19. (Previously Presented) A display device according to claim 17, wherein said source driver circuit is a digital driver with a D/A conversion circuit.

20. (Previously Presented) A display device according to claim 17, wherein said image signal processing circuit comprises a correction circuit and an A/D conversion circuit.

 21. (Previously Presented) A display device according to claim 17, wherein said display device is one selected from the group consisting of a projector, a goggle type display, a mobile computer, a video camera, a DVD player, and a game machine.

22. (Currently Amended) A display device comprising:
a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a digital video signal dividing circuit, a source driver circuit, and a gate driver circuit, wherein each circuit is formed over a same substrate as said pixel portion;
an image signal processing circuit for processing an image signal input from an external source; and
a control circuit which directly feeds pulses to said ~~display panel~~ digital video signal dividing circuit, said source driver circuit, said gate driver circuit and said image signal processing circuit,


wherein said image signal processing circuit performs gamma correction on said image signal on a basis of a correction table and feeds said digital video signal dividing circuit with said image signal on which gamma correction has been performed[[]], and

wherein said digital video signal dividing circuit feeds said source driver circuit with a digital video signal.

23. (Previously Presented) A display device according to claim 22, wherein said display panel is a liquid crystal display panel.

24. (Previously Presented) A display device according to claim 22, wherein said source driver circuit is a digital driver with a D/A conversion circuit.

25. (Previously Presented) A display device according to claim 22, wherein said image signal processing circuit comprises a correction circuit and an A/D conversion circuit.

 26. (Previously Presented) A display device according to claim 22, wherein said display device is one selected from the group consisting of a projector, a goggle type display, a mobile computer, a video camera, a DVD player, and a game machine.

27. (Previously Presented) A display device according to claim 1, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.

28. (Previously Presented) A display device according to claim 6, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.

29. (Previously Presented) A method according to claim 11, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.

30. (Previously Presented) A method according to claim 14, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.

31. (Previously Presented) A display device according to claim 17, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.

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one 32. (Previously Presented) A display device according to claim 22, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.
